

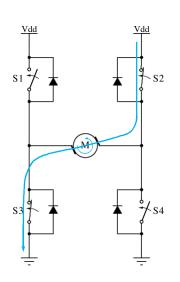
H-Bridge (application to DC motor control)

To run the motor in one direction, say clockwise, close S1 and S4.

To run the motor in the other direction, say counterclockwise, close S2 and S3.

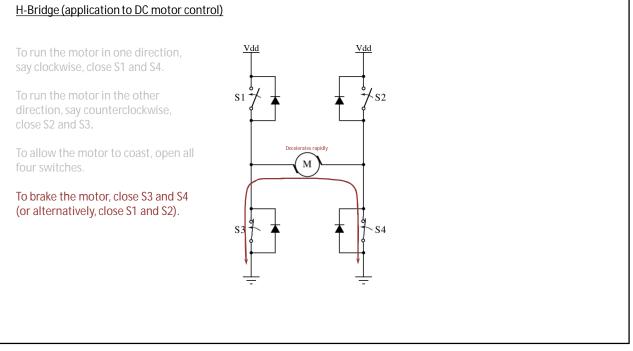
To allow the motor to coast, open all four switches.

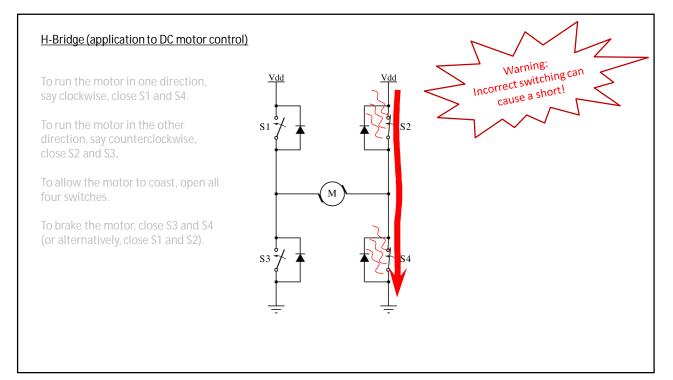
To brake the motor, close S3 and S4 (or alternatively, close S1 and S2).

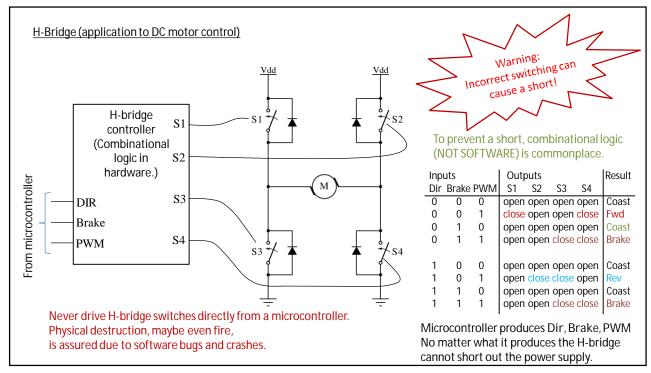


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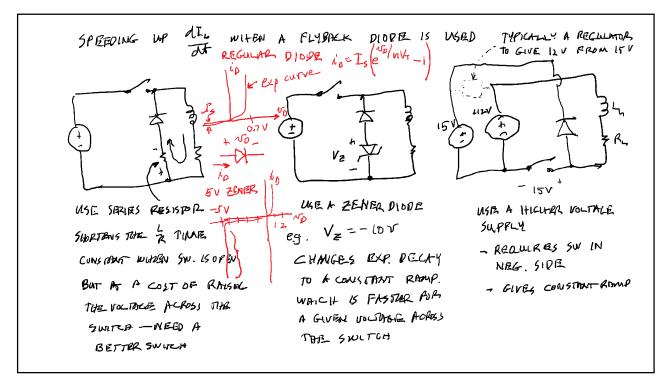
H-Bridge (application to DC motor control) Vdd Vdd To run the motor in one direction, say clockwise, close S1 and S4. To run the motor in the other S1S2 direction, say counterclockwise, close S2 and S3. To allow the motor to coast, open all М four switches. To brake the motor, close S3 and S4 (or alternatively, close S1 and S2). **S**3 **S**4

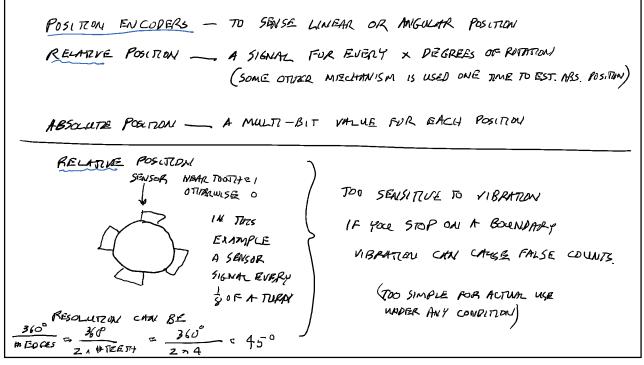


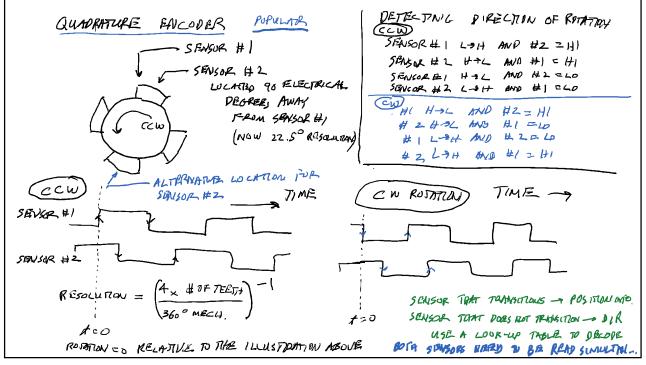


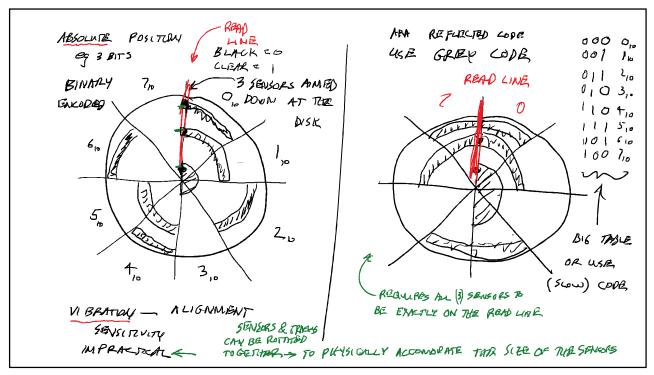


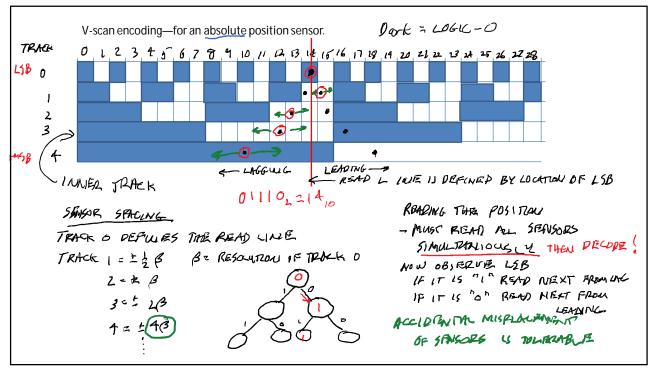


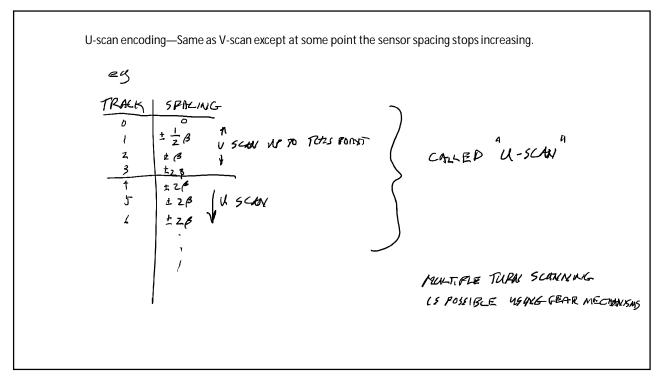




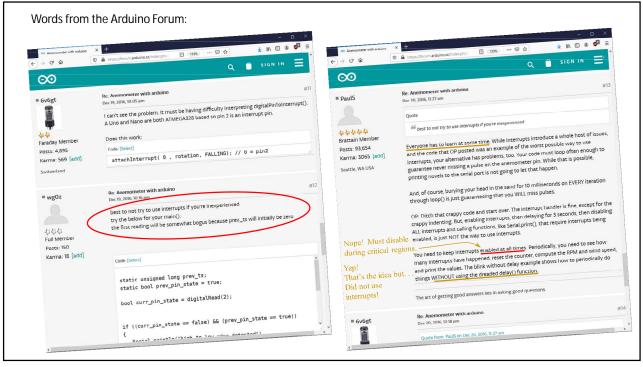












It is a two-step process This slide repeate 1.) Setup: place bits in various control registers to establish direction of I/O, input or output if input, and no connection to it, default high (enable pull up resistor), or default low (if availa if output, what is the initial output before the first write after power-up?, 1, 0 or X	
 2.) Do the actual I/O. There are <u>various strategies</u> a.) Blind-cycle: Just do it immediately as the code runs. Not in coordination with the I/O de (Ready or not, hear I come!) Gadfly: an annoying person (among other meanings) b.) Busy-waiting (aka gadfly I/O): Use a status bit to check the I/O device before reading or to it. Result: while I/O device is busy, CPU needs to wait and monitor, hence the name. (The CPU is analogous to kids in the back seat, "Are we there yet? Are we there yet? The kids don't do homework while waiting, they busy themselves only with the pest 	writing Are we there yet?"
c.) <i>Periodic polling</i> : Similar to Busy-waiting, but CPU may work on other threads of code wh waiting on a busy I/O device. (Requires a timer interrupt—i.e requires additional hardwa (The kids do homework while waiting. Every five minutes a bell rings and they ask th	are.)
 d.) Interrupt driven: The I/O device has a method in hardware to request I/O service. (The kids stick to their homework until told that they have arrived at their destination e.) Direct Memory Access: The I/O device takes over the CPU bus and writes directly into me without CPU supervision. (The kids are not in the car!) 	

An example to give some context Memory capabilities needed for subroutines (functions, procedures, interrupts, are types of subroutines) Sources of interrupts including counter-timer systems Advantages of using interrupt-driven I/O—so obvious this section is hardly needed. Alternatives to interrupt driven I/O are gadfly (uncontrolled—annoying) I/O or various polling technique all of which waste processor cycles prodigiously. Interrupts are foundational to object-oriented programming Many embedded systems that use interrupts have very little other code to run! Risks of interrupt-driven I/O density limit latency and resolution limits interval restrictions critical regions in code deadlock	The ager	nda—understanding interrupt-driven I/O (and by extension, multitasking)
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